DESCRIPTION

The IS31PM3426 regulator is a fully integrated and high frequency synchronous step-down DC-DC converter that can drive a load current of up to 2A. It can operate within an input voltage range of 3.8V to 36V. The IS31PM3426 provides exceptional efficiency, output accuracy and drop-out voltage in a very small solution size. Constant on-time control mode is employed to achieve simple control-loop compensation and fast transient response. The IS31PM3426 supports both Forced Continuous Conduction Mode (FCCM) and Pulse Frequency Modulation mode (PFM) at light-load condition, which is selected by the FPWM pin. It requires few external components. Pin arrangement allows simple, optimum PCB layout. Protection features include thermal shutdown, VDD under-voltage lockout, cycle-by-cycle current limit, over-voltage and output short-circuit protection.

The IS31PM3426 device is available in the WFCQFN-14 (3mm × 4mm) package.

FEATURES

- Input voltage range from 3.8V to 36V
- 1μA (Typ.) shutdown current
- 25μA quiescent current (typical, no switching)
- Up to 2A output current capability
- Adjustable output voltage, 1V to 24V
- Output regulation accuracy: ±1%
- 95% efficiency at full load (5V/2A)
- >91% efficiency at light load (5V/100mA)
- Integrated 80mΩ High-Side and 40mΩ Low-Side MOSFETs
- Operating frequency range: 100kHz to 2.2MHz
  - Programmed by a single resistor
  - Synchronized to external clock
- Pin-selectable FCCM or PFM operation mode
- Spread spectrum to minimize EMI
- Few external components
  - Internal loop compensation
  - Internal soft-start
- Power good flag output
- Fault protections
  - Cycle by cycle current limit
  - Precision enable to program system UVLO
  - Output short-circuit protection with hiccup mode
  - Output over-voltage protection
  - VDD under-voltage lockout
  - Thermal shutdown protection
- WFCQFN-14 (3mm × 4mm) compact package
- RoHS & Halogen-Free Compliance
- TSCA Compliance

QUICK START

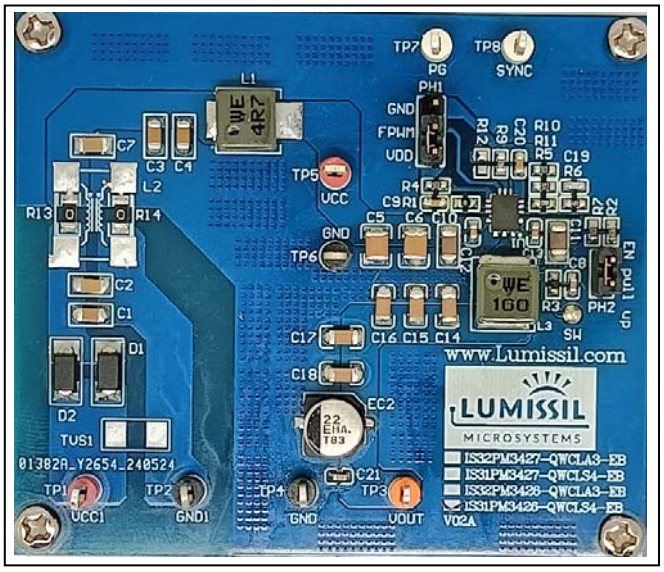

Figure 1 *Photo of IS31PM3426 Evaluation Board*

APPLICATIONS

- General-purpose power supply

DEMO INFORMATIONS

- Input: 9V~16VDC
- Output: 5V/0~2A

Note: If the output voltage set by the demo board is not what you want, you can change the output voltage you want through the design method described below.

ABSOLUTE MAXIMUM RATINGS

- Input voltage ≤ 36VDC

Caution: Do not exceed the conditions listed above, otherwise the board will be damaged.

PROCEDURE

The IS31PM3426 DEMO Board is fully assembled and tested. Follow the steps listed below to verify board operation.

Caution: Do not turn on the power supply until all connections are completed.

1) Connect the positive terminal of the power supply to the VCC1 of the board and the negative terminal of the power supply to the GND1 of the board.
2) Connect the electronic load (or adjustable resistor load) to the VOUT terminal and GND terminal of board.
3) Connect the PH2 to pull up EN pin, the chip will be enabled.

4) The PG terminal on the board is an output voltage error reporting function, which is pulled up to VDD by a resistor during normal operation. Once the output voltage is below 93% × VOUT or 107% VOUT high, the PG will be pulled down.

5) This demo board is set to work in FCCM mode by default when the chip is under light load. If you want to change it to PFM mode, please use a jump cap to connect PH1, let FPWM pin short to GND.

ORDER INFORMATION

Part No.	Temperature Range	Package
IS31PM3426-QWCLS4-EB	-40°C to +125°C (Industrial)	WFCQFN-14, Lead free

Table 1 *Ordering Information*

For pricing, delivery, and ordering information, please contacts Lumissil's analog marketing team at analog@Lumissil.com or (408) 969-6600.

DESIGN INFORMATION

SETTING THE OUTPUT VOLTAGE

The external resistor divider, R_{FBT} and R_{FBB}, is used to set the output voltage (As shown in Figure 2), according to the following equation:

$$V_{OUT} = \frac{(R_{FBB}+R_{FBT})}{R_{FBB}} \times V_{FB\_TH} \qquad (1)$$

Where V_{FB_TH}=1V (Typ.).

Choosing a value for the resistor R_{FBB} should be reasonable. Usually, a small R_{FBB} leads to considerable quiescent current loss, while a large R_{FBB} makes the FB pin noise-sensitive and voltage errors from the V_{FB} input current are noticeable.

In order to have an accurate output voltage, precision resistors are preferred (±1% recommended). The R_{FBT} and R_{FBB} resistors should be placed as close as possible to the IS31PM3426 with minimal trace length to the FB and AGND pins.

In addition, it should be noted that the resistor R_T and capacitor C_{FF} are required. The R_T is fixed at 1kΩ, while the C_{FF} is fixed at 33pF.

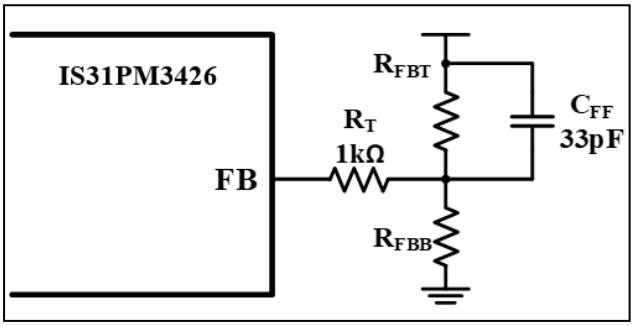

Figure 2 Feedback Network

Table 1 R_{FB} Resistance Versus Output Voltage

V_{OUT} (V)	R_{FBB} (kΩ)	R_{FBT} (kΩ)
3.3	43.5	100
5	25	100
8	14.3	100

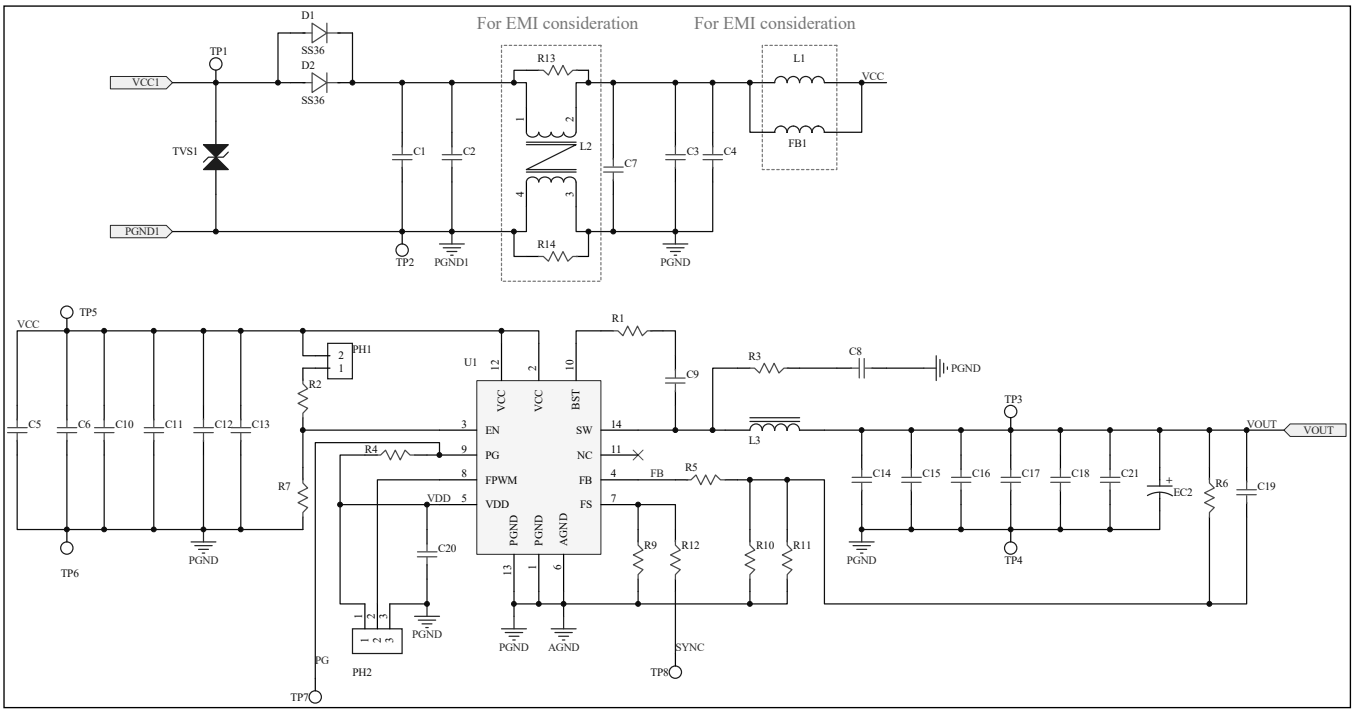

Figure 3 *IS31PM3426 Demo Board Schematic*

BILL OF MATERIALS

Name	Symbol	Description	Qty	Supplier	Part No.
IC	U1	Constant voltage chip	1	Lumissil	IS31PM3426-QWCLS4-TR
Capacitor	C1, C3, C4	CAP,2.2μF,50V, ±10%, SMD	3	Yageo	CC1206KKX7R9BB225
Capacitor	C2, C7	CAP,100nF,50V, ±10%, SMD	2	Yageo	CC1206KRX7R9BB104
Capacitor	C5, C6	CAP,10μF,50V, ±10%, SMD	2	Yageo	CC1210KKX7R9BB106
Capacitor	C10, C11	CAP,1μF,50V, ±10%, SMD	2	Yageo	CC1206KKX7R8BB105
Capacitor	C9, C12, C13, C21	CAP,0.1μF,50V, ±10%, SMD	4	Yageo	CC0603KRX7R9BB104
Capacitor	C20	CAP,1μF,16V, ±10%, SMD	1	Yageo	CC0603KRX7R7BB105
Capacitor	C8	CAP,1nF,50V, ±10%, SMD	1	Yageo	CC0603KRX7R9BB102
Capacitor	C19	CAP,33pF,25V, ±10%, SMD	1	Yageo	CC0603JRNPO8BN330
Capacitor	C14, C15, C16, C17, C18	CAP,4.7μF,25V, ±10%, SMD	5	Yageo	CC1206KKX7R8BB475
Resistor	R2, R4, R7	RES,47k,1/10W, ±5%, SMD	3	Yageo	RC0603JR-0747KL
Resistor	R1	RES,0R, 1/10W, ±5%, SMD	1	Yageo	RC0603JR-070RL
Resistor	R3	RES,2R, 1/10W, ±5%, SMD	1	Yageo	RC0603JR-072RL
Resistor	R5	RES,1k, 1/10W, ±1%, SMD	1	Yageo	RC0603FR-071KL
Resistor	R9	RES,82k,1/10W, ±1%, SMD	1	Yageo	RC0603FR-0782KL
Resistor	R10	RES,47k,1/10W, ±1%, SMD	1	Yageo	RC0603FR-0747KL
Resistor	R11	RES,51k,1/10W, ±1%, SMD	1	Yageo	RC0603FR-0751KL
Resistor	R13, R14	RES,0R, 1/4W, ±1%, SMD	2	Yageo	RC1206FR-070RL
Resistor	R6	RES,100k,1/10W, ±1%, SMD	1	Yageo	RC0603FR-07100KL
Resistor	R12	NC			
Diode	D1, D2	3A,60V, SMA	2	Diodes	SS36
SMD Inductor	L1	4.7μH±20%, Isat=5.9A	1	Wurth	74437336047
Headers	PH1, PH2	2 pin, 3pin Headers 2.54mm	2		
Terminal	VCC1, GND1, VCC, GND*2, VOUT, SYNC, PG		8		
Inductor	L3	10μH±20%, Isat=5.05A,10% SMD	1	Wurth	78439346100
SMD Inductor	L2	NC			
Capacitor	EC2	E-CAP,22μF,25V, ±10%, SMD	1	Panasonic	EEEHA1E220P
SMD Magnetic Bead	FB1	NC			
SMD TVS	TVS1	NC			

Note: Bill of materials refers to Figure 3 above.

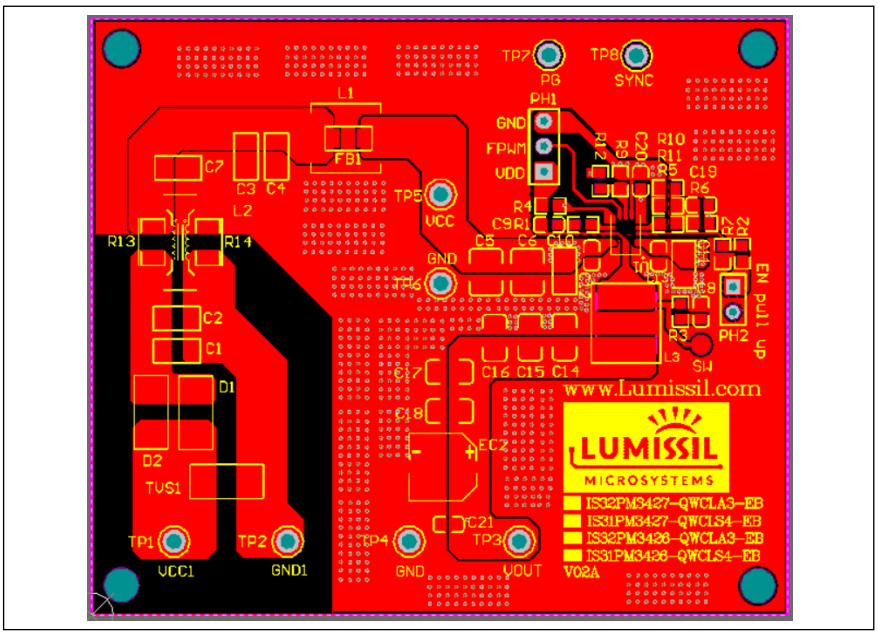

Figure 4 *Board PCB Layout - Top Layer*

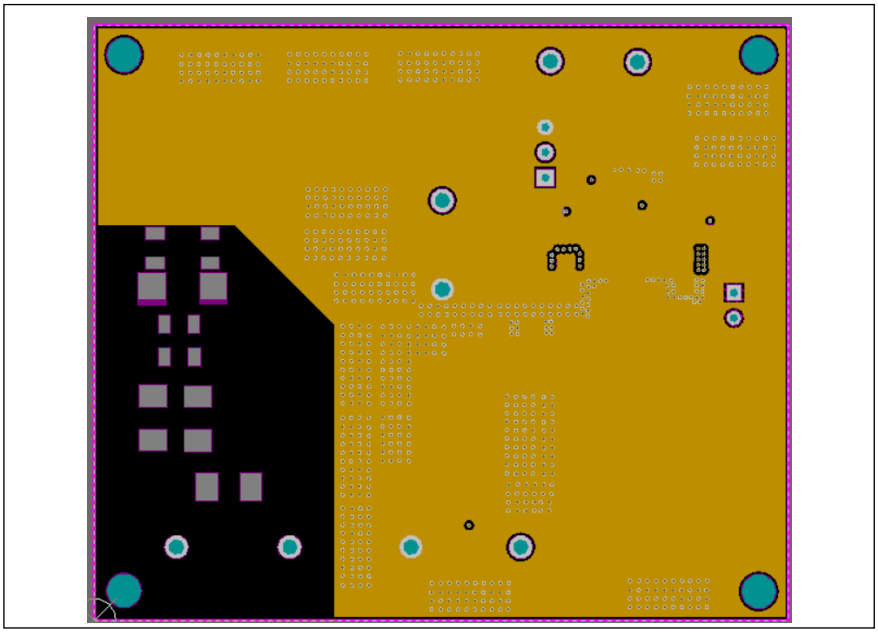

Figure 5 *Board PCB Layout - Mid-Layer 1*

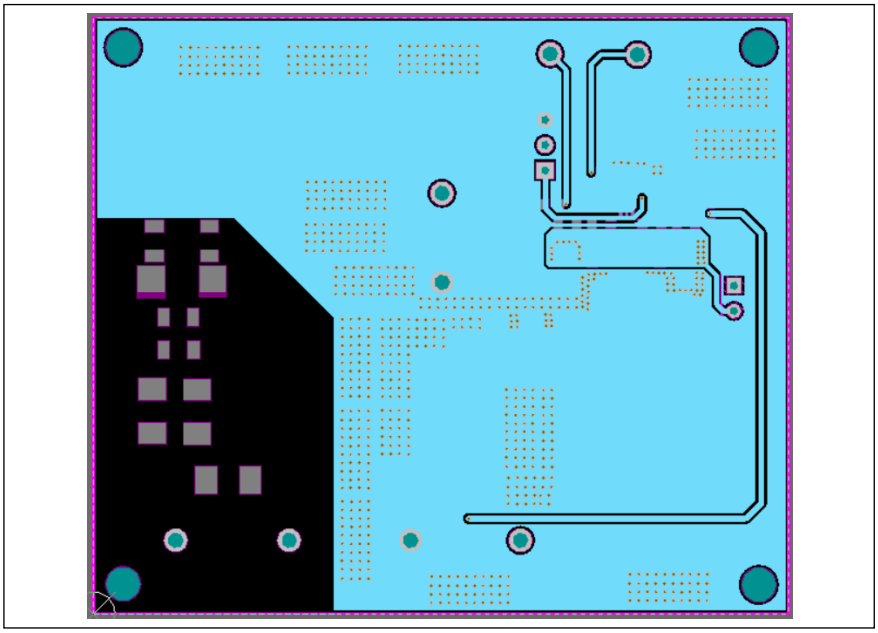

Figure 6 *Board PCB Layout - Mid-Layer 2*

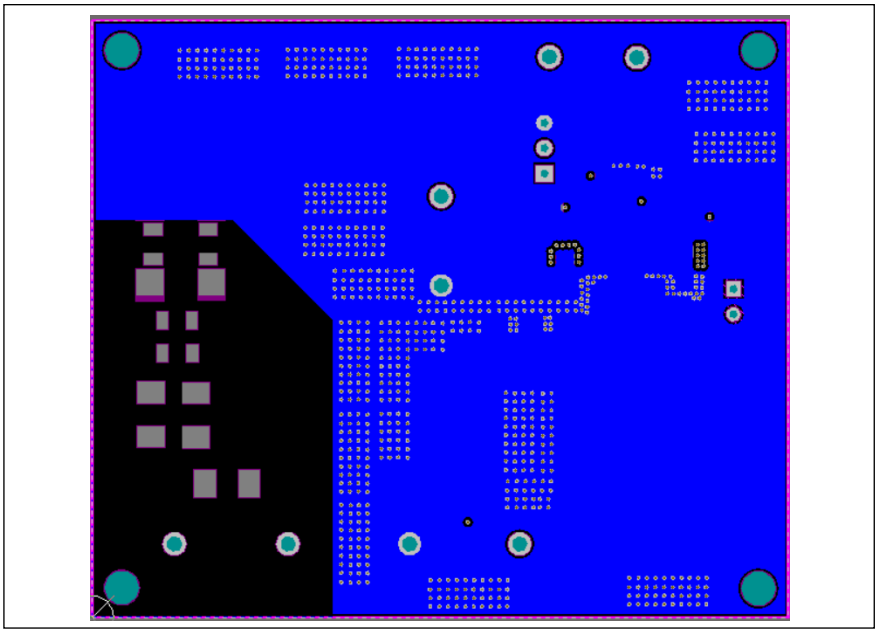

Figure 7 *Board PCB Layout - Bottom Layer*

CONDUCTED EMI PERFORMANCE

Test condition: V_{CC}= 12V, V_{OUT}= 5V, I_{OUT}= 2A, f_{SW}= 400kHz, naked board without any shielding.

Test standard: CISPR-25 Class 5 conducted EMI (Blue: Peak scanning, Red: Average scanning).

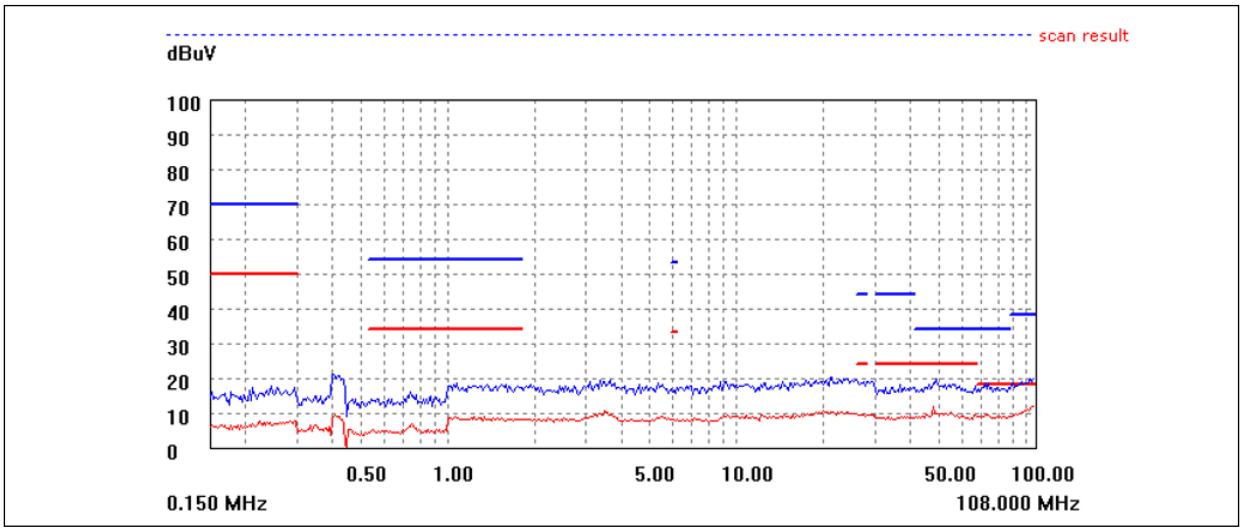

Figure 8 *CISPR-25 Class 5 Conducted EMI Scan (VCC)*

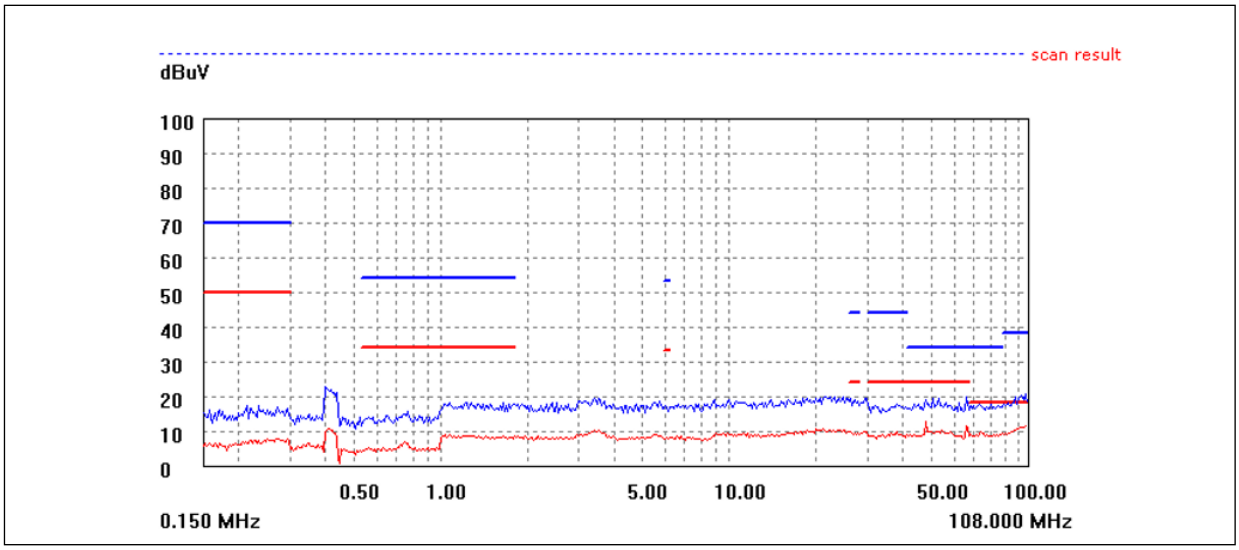

Figure 9 *CISPR-25 Class 5 Conducted EMI Scan (GND)*

REVISION HISTORY

Revision	Detail Information	Date
A	Initial release	2024.08.08